

FIG. 1

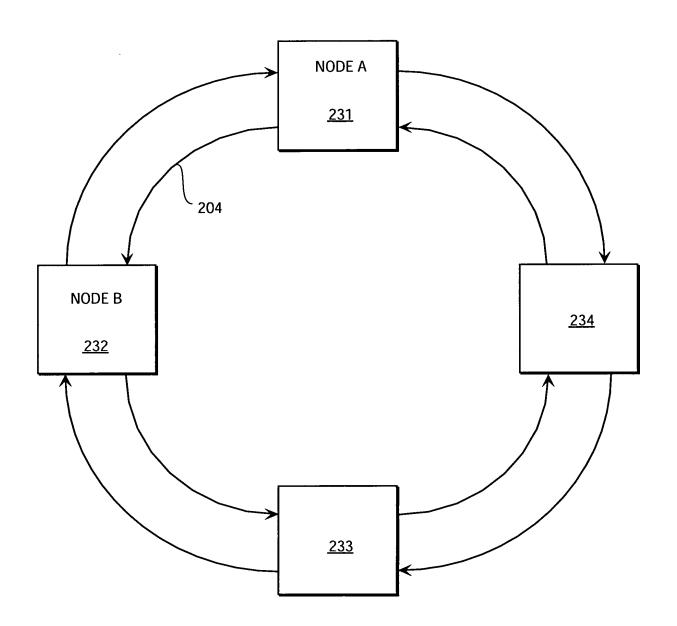


FIG. 2

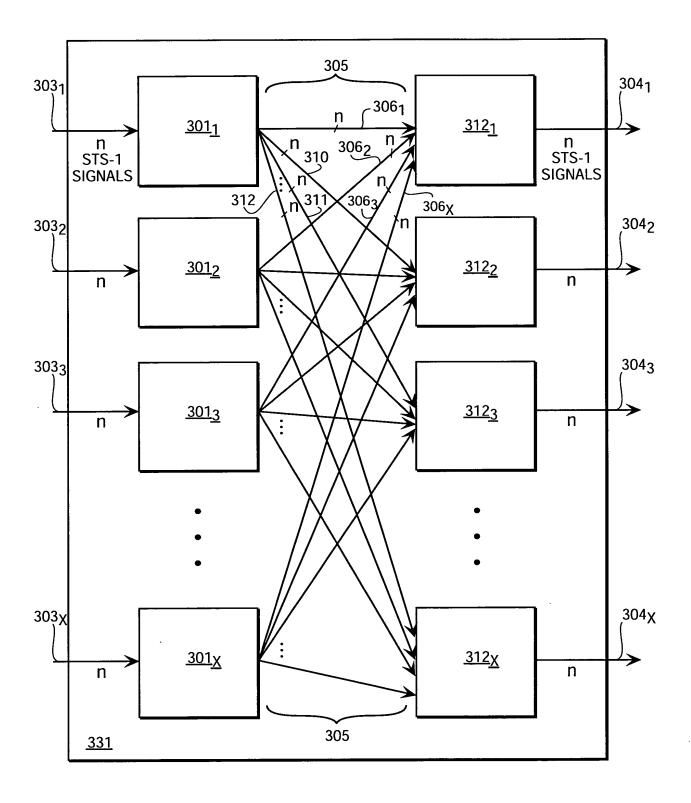


FIG. 3

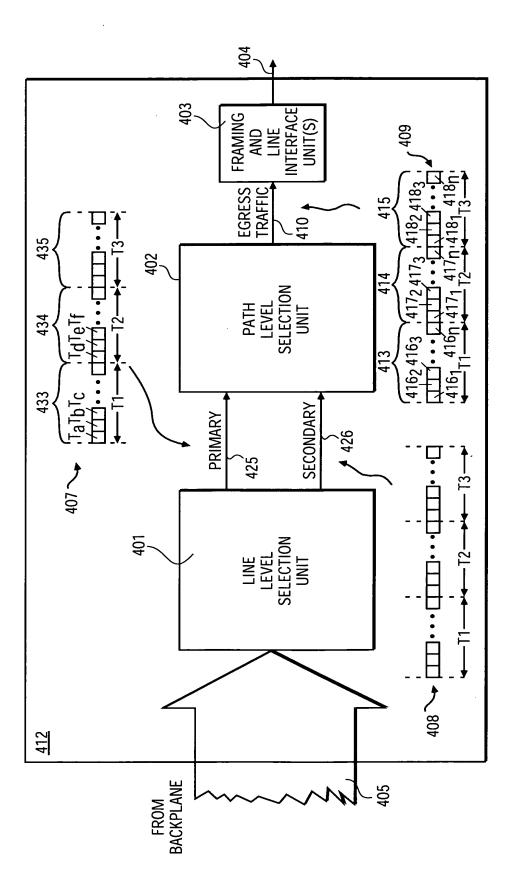


FIG. 4

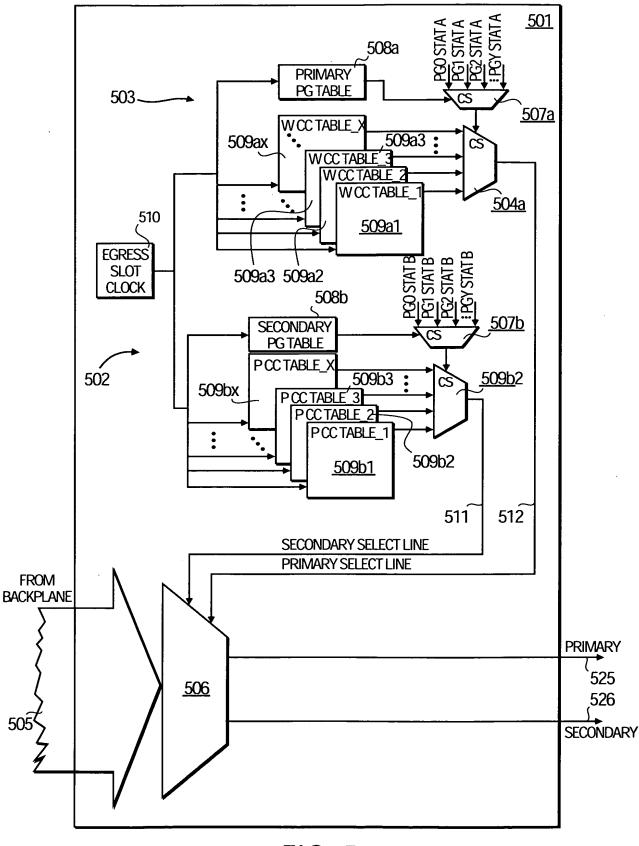


FIG. 5

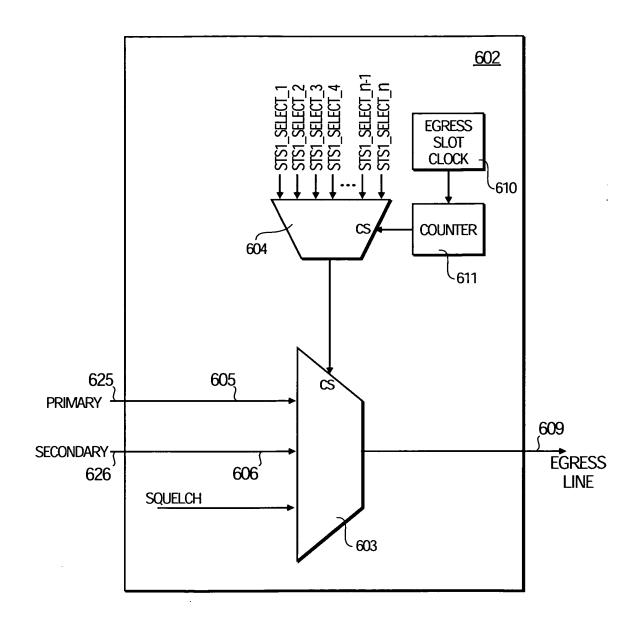


FIG. 6

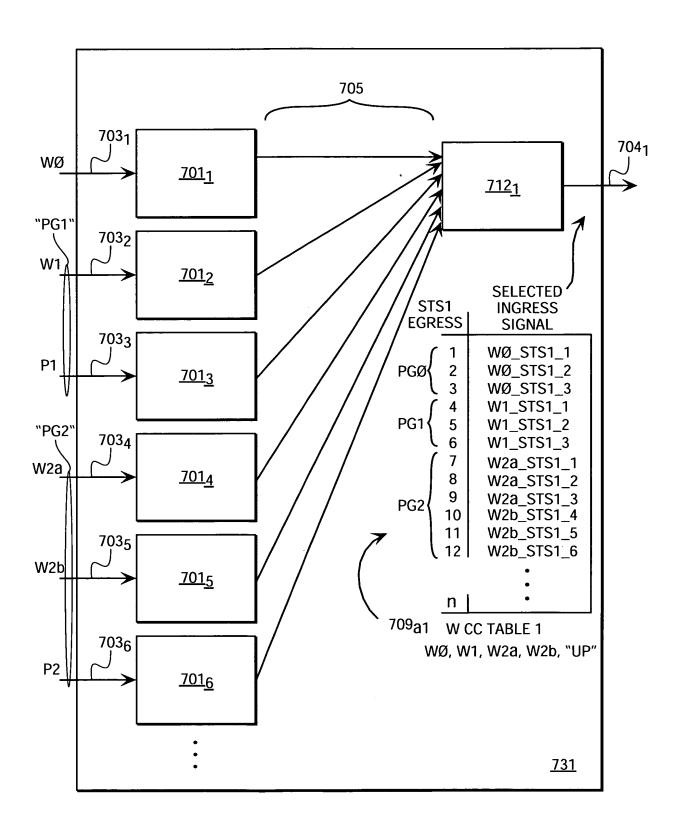


FIG. 7

				PG1 PROTECTION	(W1 DOWN)	W CC TABLE_2	809a2	
SELECTED INGRESS SIGNAL				P1_STS1_1	P1_STS1_2	P1_STS1_3	• • •	
STS1 EGRESS	1	2	3	4	2	9		Ľ

FIG. 8∆

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						PG2 PROTECTION E			W CC TABLE_4							
SELECTED INGRESS SIGNAL							W2a_STS1_1	W2a_STS1_2	W2a_STS1_3	P2_STS1_4	P2_STS1_5	P2_STS1_6	•	• (•	
STS1 EGRESS	-	2	3	4	2	9	7	8	6	10	11	12				u
						PG2 PROTECTION A		1 1 1 1 1	W CC ABLE_3 809a3							
SELECTED INGRESS SIGNAL							P2_STS1_1	P2_STS1_2	W2b_STS1_3	W2b_STS1_4	W2b_STS1_5	W2b_STS1_6	•	• •	•	
STS1 EGRESS	1	2	3	4	5	9	7	8	6	10	11	12				L

FIG. 8B

STS1 EGRESS	APPLICABLE PROTECTION GROUP
1	PGØ
2	PGØ
3	PGØ
4	PG1
5	PG1
6	PG1
7	PG2
8	PG2
9	PG2
10	PG2
11	PG2
12	PG2
	•
n	

PRIMARY PG TABLE 908a

FIG. 9A

LII	NE STATUS	PGØ STAT A	PG1 STAT A	PG2 STAT A	
WØ, W1,	W2a, W2b, "UP"	1	1	1	
W1	"DOWN"	1	2	1	
W28	a "DOWN"	1	1	3	
W2I	b "DOWN"	1	1	4	
W1 "DOW	/N", W2a "DOWN"	1	2	3	
W1 "DOW	/N", W2b "DOWN"	1	2	4	
W1 "DOWN", W2a "DOW	/N", W2b "DOWN"	1	2	3	
W2a "DOWN	", W2b "DOWN"	1	1	3	
		•	•		

FIG. 9B

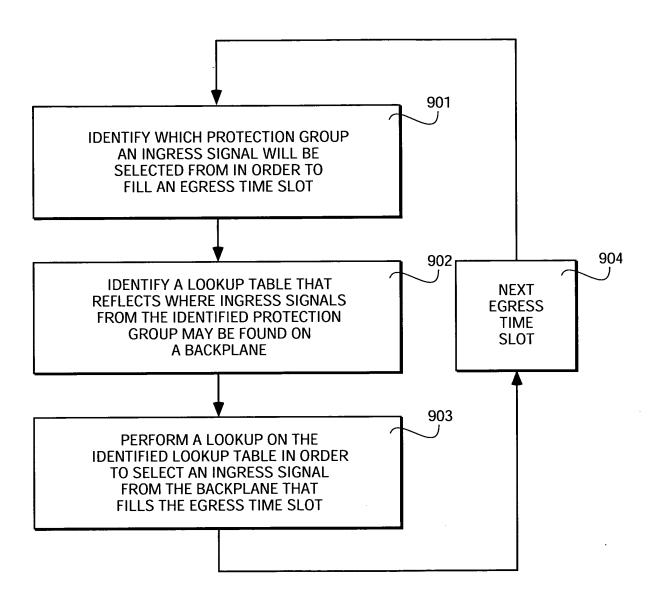


FIG. 9C

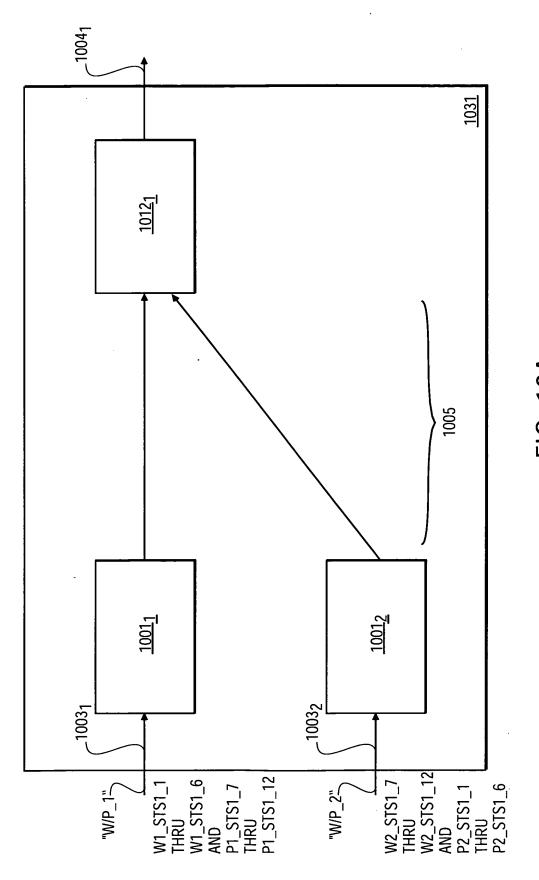
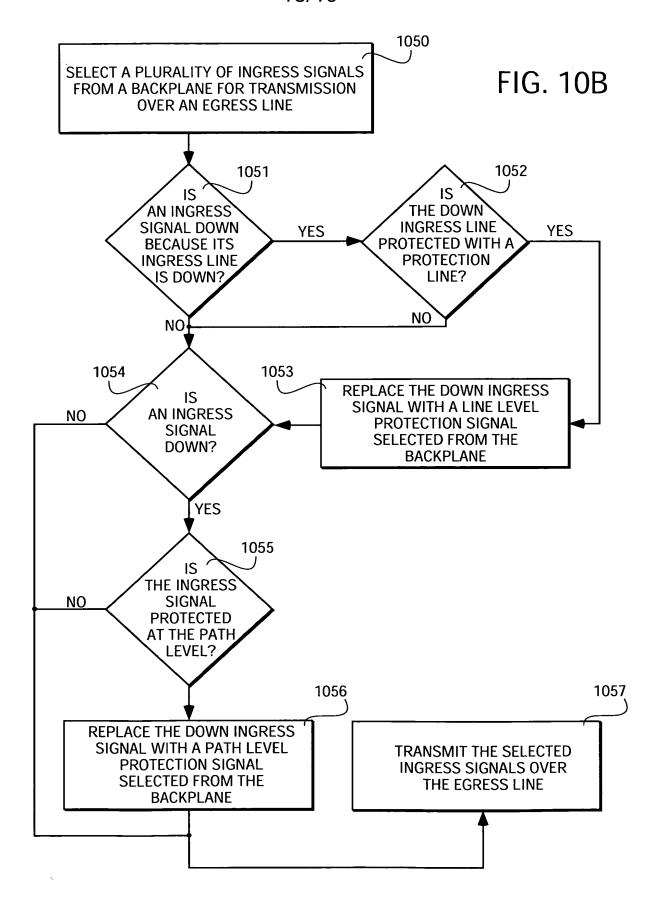


FIG. 10A



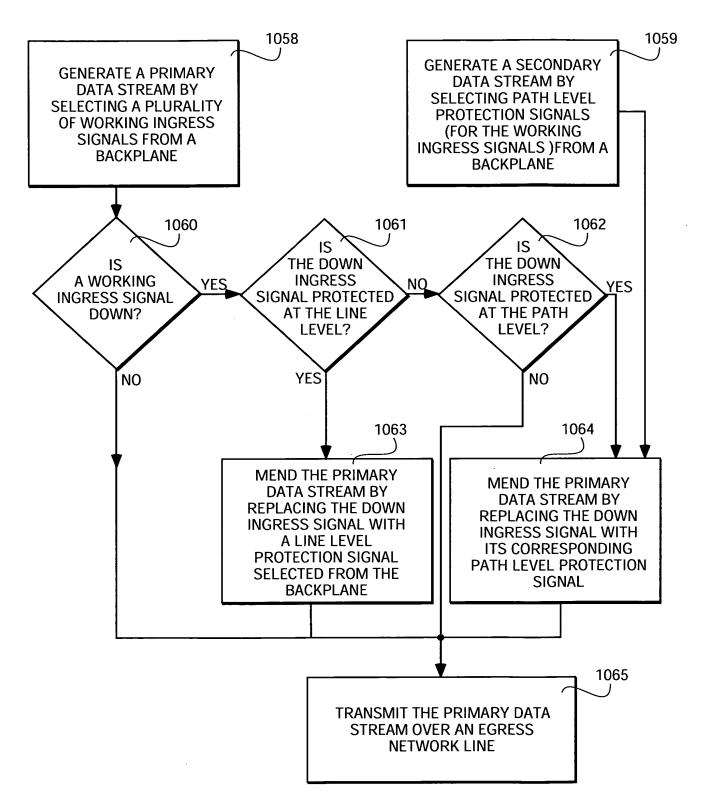


FIG. 10C

1	STS1 EGRESS	SELECTED INGRESS SIGNAL		
3 W1_STS1_3 4 W1_STS1_4 5 W1_STS1_5 6 W1_STS1_6 7 W2_STS1_7 8 W2_STS1_8 9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	1	W1_STS1_1		
4 W1_STS1_4 5 W1_STS1_5 6 W1_STS1_6 7 W2_STS1_7 8 W2_STS1_8 9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	2	W1_STS1_2		
5 W1_STS1_5 6 W1_STS1_6 7 W2_STS1_7 8 W2_STS1_8 9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	3	W1_STS1_3		
6 7 8 W2_STS1_7 8 W2_STS1_8 9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	4	W1_STS1_4		
7	5	W1_STS1_5		
8 W2_STS1_8 9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	6	W1_STS1_6	★	
9 W2_STS1_9 10 W2_STS1_10 11 W2_STS1_11	7	W2_STS1_7	l \	
10 W2_STS1_1Ø 11 W2_STS1_11	8	W2_STS1_8	1109a1	FIG. 11A
11 W2_STS1_11	9	W2_STS1_9		
	10	W2_STS1_1Ø		
12 W2_STS1_12	11	W2_STS1_11		
:	12	W2_STS1_12		
•		•		
n	n			

STS1 EGRESS	SELECTED INGRESS SIGNAL		
1	P2_STS1_1	7	
2	P2_STS1_2		
3	P2_STS1_3		
4	P2_STS1_4		
5	P2_STS1_5		
6	P2_STS1_6	* 200 TIBLE 4	
7	P1_STS1_7	P CC TABLE_1 1109b1	FIC 11D
8	P1_STS1_8	110901	FIG. 11B
9	P1_STS1_9		
10	P1_STS1_1Ø		
11	P1_STS1_11		
12	P1_STS1_12		
	•		
n i			

STS1_SELECT_n	M	8	>	·
STS1_SELECT_12	8	≥	≽	
STS1_SELECT_11	×	≥	۵	
STS1_SELECT_1Ø	8	>	≽	
STS1_SELECT_9	>	≥	≯	
STS1_SELECT_8	8	>	≯	
STS1_SELECT_7	M	3	≯	
STS1_SELECT_6	8	≥	≽	• • •
STS1_SELECT_5	8	≥	۵	
STS1_SELECT_4	×	≥	≯	
STS1_SELECT_3	8	≥	≽	
STS1_SELECT_2	×	≥	≽	-
STS1_SELECT_1	M	۵.	>	
PATH STATUS	* ALL WORKING PATHS "UP"	* W1_STS1_1 "DOWN"	* W1_STS1_5 AND W_STS1_11 "DOWN"	

FIG. 12

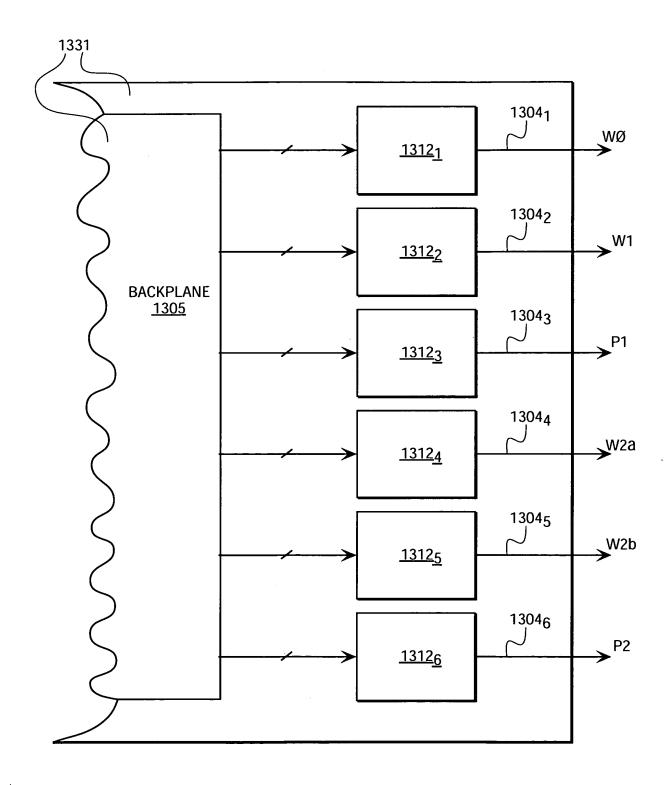


FIG. 13

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2a_STS1_W_1
2	W2a_STS1_W_2
3	W2a_STS1_W_3
4	W2a_STS1_W_4
	•
n	W2a_STS1_W_n
	W_CC_TABLE_1 1409a1

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2a_STS1_P_1
2	W2a_STS1_P_2
3	W2a_STS1_P_3
4	W2a_STS1_P_4
	•
n	W2a_STS1_P_n

W_CC_TABLE_2 1409a2

FIG. 14A

FIG. 14B

STS EGRESS	SELECTED INGRESS SIGNAL
1	W2b_STS1_W_1
2	W2b_STS1_W_2
3	W2b_STS1_W_3
4	W2b_STS1_W_4
	•
n	W2b_STS1_W_n
	♦ W_CC_TABLE_3

 STS EGRESS
 SELECTED INGRESS SIGNAL

 1
 W2b_STS1_P_1

 2
 W2b_STS1_P_2

 3
 W2b_STS1_P_3

 4
 W2b_STS1_P_4

 •
 •

 0
 •

 1
 W2b_STS1_P_n

W_CC_TABLE_4 1409a4

FIG. 14C

- 1409a3

FIG. 14D

